### **XDS100V2**



### **Product description**

DSP emulator

XDS100V2 official version

Support TI DSP ARM

CCSV4 and higher versions are supported, CCSV3.3 is not supported!

Simulation debugger XDS100V2 supports DSP TMS320F28335 upgraded high-speed interface

## 1 产品介绍

XDS100V2 仿真器是XDS100V1仿真器的升级版,是 XDS100 JTAG仿真技术的第二个版本,与XDS100V1相比 XDS100V2的速度更快,支持的DSP芯片型号也更多,还支 持TI的ARM芯片,是一款性价比非常高的DSP仿真器。

XDS100V2 仿真器支持USB2.0高速接口,通过14PIN的接口进行仿真调试,支持Code Composer Studio (CCS) V4 和更高的版本。

### • 产品特点

- <1>USB2.0 高速接□(480 Mbit/s)
- <2>14PIN的标准JTAG接口
- <3>支持高速USB代码下载功能
- 〈4〉支持断电检测
- 〈5〉支持多种 FTDI 设备驱动
- 〈6〉支持自适应时钟
- <7>支持的Code Composer Studio v4(CCS4)和更高版本
- <8>支持LED灯显示控制USB连接情况

## 1 支持芯片 (

### ✓TMS320C28x 系列

常见有: TMS320F2812、 TMS320F2810 、TMS320F28335、

TMS320F2802、TMS320F2806、TMS320F2808、

TMS320F2809、TMS320F28232、TMS320F28027、

TMS320F28035 等等

✓TMS320C54x 系列

常见有: TMS320C5402、TMS320C5409、TMS320C5416等

✓TMS320C55x 系列

常见有: TMS320C5502、TMS320C5509、TMS320C5509A等

✓TMS320C64x+系列

常见有: TMS320C6421、TMS320C6424、TMS320C6455等

✓TMS320C674x系列

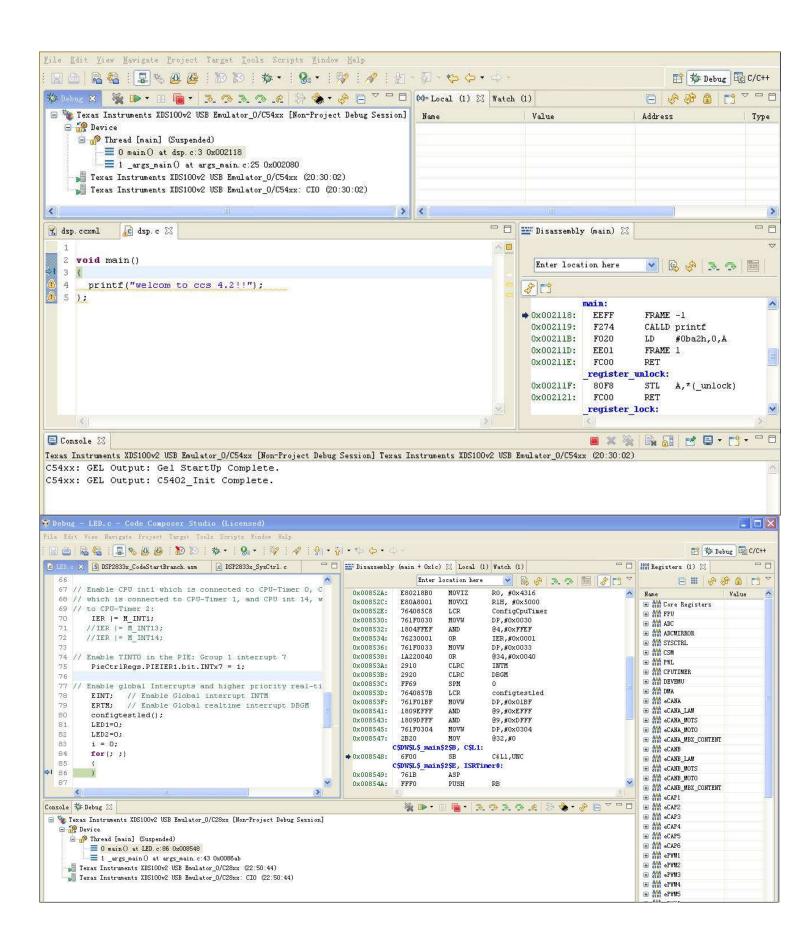
常见有: TMS320C6745、TMS320C6747、TMS320C6748等

✓ARM 9系列

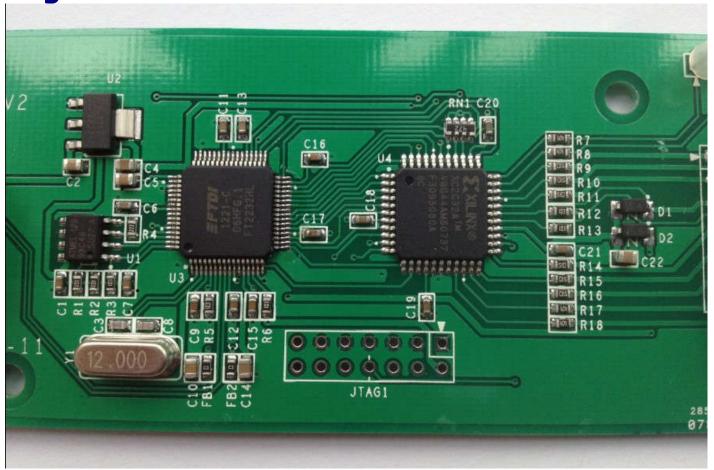
✓ARM Cortex R4系列

✓ARM Cortex A8系列

# Support WINXP/7/10 32&64bit Simulation debugging:



The internal circuit is exactly the same as the original:



**Packing List:** 

1. 1 host

2. 1 USB cable as a gift

3. One 14P gray cable is given as a gift

The CCS software can be downloaded from the TI official website (you can register an account), and the driver is integrated (copied to the browser):

http://processors.wiki.ti.com/index.php/Download\_CCS Reference material download address (copy to browser): http://pan.baidu.com/s/1mgDQeUW

### **Support Chip:**

- 1, TMS320C28x series
- 2, TMS320C54x series
- 3, TMS320C55x series
- 4, TMS320C64x + Series
- 5, TMS320C674x series
- 6, TMS320C66x series
- 7, ARM 9 Series
- 8, ARM Cortex A9 Series
- 9, ARM Cortex A8 Series
- 10, ARM Cortex M3/M4 Series
- 11, ARM Cortex R4 Series

#### **Product Features:**

- 1, debug function ( connect / disconnect , read / write memory , read registers, loader , run / stop steps to support the breakpoint debugging , real-time mode ) .
- 2, USB2.0 high speed interface, compatible with USB 2.0 full-speed, and low-speed USB1.1.
- 3,14 PIN standard JTAG interface, support for TI chip processor.
- 4, support Hi-Speed USB code download.
- 5, support for power-on reset startup mode, JTAG reset, wait and then reset the startup mode.
- 6, support for outage detection.
- 7, support for multiple FTDI device driver.
- 8, support adaptive clock, adaptive target board voltage.
- 9, support for Code Composer Studio v4 and later versions, the latest version CCS5.2 has passed rigorous testing.
- 10 LED lights display controller supports USB connections.
- 11, SRV05-4 do ESD protection . Protect expensive DSP chip to prevent static electricity from the human body burn JTAG DSP chip .

## FY-XDS100V2 仿真器输出接口:

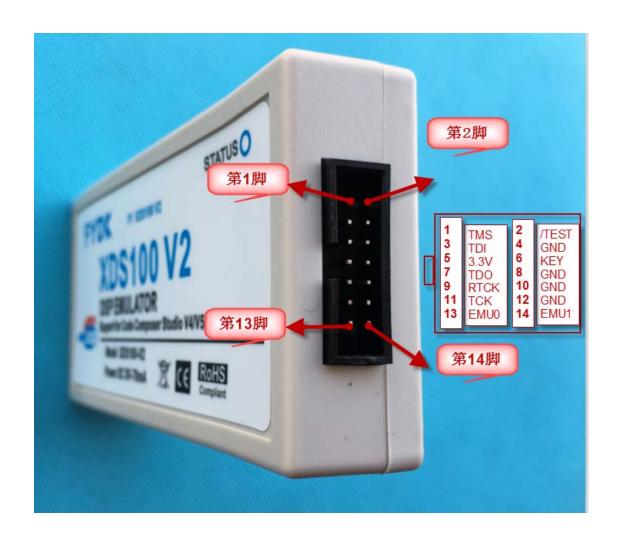
设备输出默认标准的 14 针 JTAG 口 定义如下:

| TMS       | 1  | 2  | /TEST |
|-----------|----|----|-------|
| TDI       | 3  | 4  | GND   |
| PD(+3.3V) | 5  | 6  | KEY   |
| TDO       | 7  | ω  | GND   |
| TCK-RET   | 9  | 10 | GND   |
| TCK       | 11 | 12 | GND   |
| EMU0      | 13 | 14 | EMU1  |

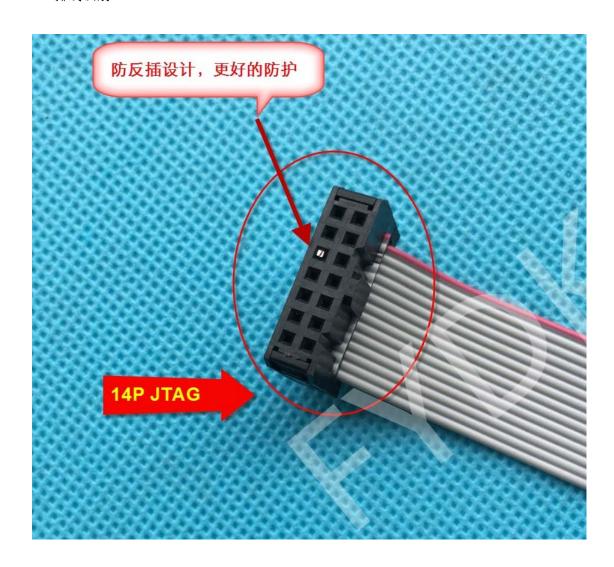
其中第 9 脚即 RTCK

注意:下载器不向外提供电源:其中 5 脚不输出电源,此脚需要接目标板的电源,否则无法工作!

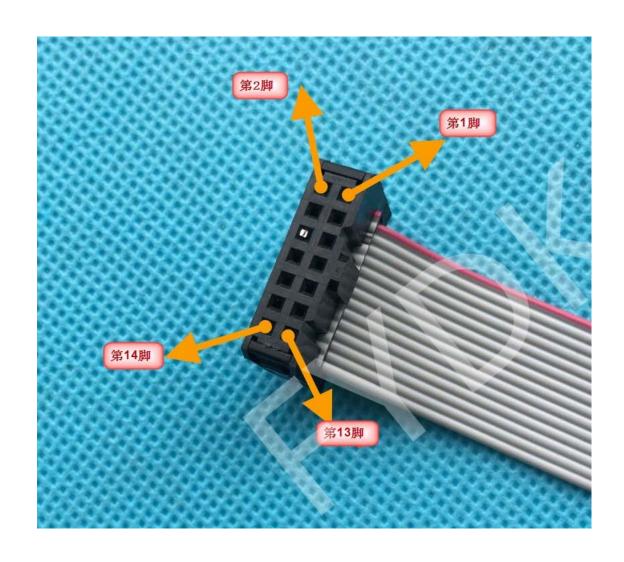
设备对应:



### 14P 排线识别:



排线输出引脚顺序:



### 如何识别第1脚:

三角形位置为 1 脚,一排顺下来是单数 1.3.5.7.9............

另一排顺下来是双数: 2.4.6.8.10.12.......

