# LM7905,LM7912,LM7915

LM79XX Series 3-Terminal Negative Regulators



Literature Number: SNOSBQ7B



### LM79XX Series

# **3-Terminal Negative Regulators**

### **General Description**

The LM79XX series of 3-terminal regulators is available with fixed output voltages of –5V, –12V, and –15V. These devices need only one external component—a compensation capacitor at the output. The LM79XX series is packaged in the TO-220 power package and is capable of supplying 1.5A of output current.

These regulators employ internal current limiting safe area protection and thermal shutdown for protection against virtually all overload conditions.

Low ground pin current of the LM79XX series allows output voltage to be easily boosted above the preset value with a

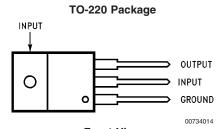
resistor divider. The low quiescent current drain of these devices with a specified maximum change with line and load ensures good regulation in the voltage boosted mode.

For applications requiring other voltages, see LM137 datasheet.

#### **Features**

- Thermal, short circuit and safe area protection
- High ripple rejection
- 1.5A output current
- 4% tolerance on preset output voltage

### Connection Diagram



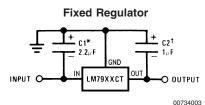
Front View
Order Number LM7905CT, LM7912CT or LM7915CT
See NS Package Number TO3B

\*Required if regulator is separated from filter capacitor by more than 3". For value given, capacitor must be solid tantalum. 25µF aluminum electrolytic may be substituted.

†Required for stability. For value given, capacitor must be solid tantalum. 25µF aluminum electrolytic may be substituted. Values given may be increased without limit.

For output capacitance in excess of  $100\mu F$ , a high current diode from input to output (1N4001, etc.) will protect the regulator from momentary input shorts.

# **Typical Applications**



## **Absolute Maximum Ratings** (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Input Voltage

Input-Output Differential

$(V_o = -5V)$	25V
$(V_o = -12V \text{ and } -15V)$	30V
Power Dissipation (Note 2)	Internally Limited
Operating Junction Temperature Range	0°C to +125°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature (Soldering, 10 sec.)	230°C

### **Electrical Characteristics**

 $Conditions \ unless \ otherwise \ noted: \ I_{OUT} = 500 mA, \ C_{IN} = 2.2 \mu F, \ C_{OUT} = 1 \mu F, \ 0^{\circ}C \leq T_{J} \leq +125^{\circ}C, \ Power \ Dissipation \leq 1.5 W.$ 

Part Number				Units		
	· · · · · · · · · · · · · · · · · · ·	Voltage	-5V		_	
	Input Voltage (unles	s otherwise specified)		-10V		
Symbol	Parameter	Conditions	Min	Тур	Max	
Vo	Output Voltage	$T_J = 25^{\circ}C$	-4.8	-5.0	-5.2	V
		$5mA \le I_{OUT} \le 1A$ ,	-4.75		-5.25	V
		P ≤ 15W		$(-20 \le V_{IN} \le -$	7)	V
$\Delta V_{O}$	Line Regulation	T <sub>J</sub> = 25°C, (Note 3)		8	50	mV
				$(-25 \le V_{IN} \le -$	7)	V
				2	15	mV
				$(-12 \le V_{IN} \le -$	8)	V
$\Delta V_{O}$	Load Regulation	T <sub>J</sub> = 25°C, (Note 3)				
		$5mA \le I_{OUT} \le 1.5A$		15	100	mV
		250mA ≤ I <sub>OUT</sub> ≤ 750mA		5	50	mV
I <sub>Q</sub>	Quiescent Current	$T_J = 25^{\circ}C$		1	2	mA
$\Delta I_Q$	Quiescent Current	With Line			0.5	mA
	Change			$(-25 \le V_{IN} \le -$	7)	V
		With Load, $5mA \le I_{OUT} \le 1A$			0.5	mA
V <sub>n</sub>	Output Noise Voltage	$T_A = 25^{\circ}C, 10Hz \le f \le 100Hz$		125		μV
	Ripple Rejection	f = 120Hz	54	66		dB
				$(-18 \le V_{IN} \le -$	8)	V
	Dropout Voltage	$T_J = 25^{\circ}C$ , $I_{OUT} = 1A$		1.1		V
I <sub>OMAX</sub>	Peak Output Current	T <sub>J</sub> = 25°C		2.2		А
	Average Temperature	I <sub>OUT</sub> = 5mA,		0.4		mV/°C
	Coefficient of	0 C ≤ T <sub>J</sub> ≤ 100°C				
	Output Voltage					

### **Electrical Characteristics**

 $Conditions \ unless \ otherwise \ noted: \ I_{OUT} = 500mA, \ C_{IN} = 2.2 \mu F, \ C_{OUT} = 1 \mu F, \ 0^{\circ}C \leq T_{J} \leq +125^{\circ}C, \ Power \ Dissipation \leq 1.5W.$ 

Units	LM7915C			LM7912C			Part Number		
7	-15V		-12V			Output Voltage			
7		-23V			-19V		otherwise specified)	Input Voltage (unless o	
7	Max	Тур	Min	Max	Тур	Min	Conditions	Parameter	Symbol
V	-15.6	-15.0	-14.4	-12.5	-12.0	-11.5	$T_J = 25^{\circ}C$	Output Voltage	Vo
i v	-15.75		-14.25	-12.6		-11.4	$5mA \le I_{OUT} \le 1A$ ,		
V	–17.5)	$\leq V_{IN} \leq -$	(-30 :	–14.5)	$\leq V_{IN} \leq 1$	(–27 ≤	P ≤ 15W		
mV	100	5		80	5		T <sub>J</sub> = 25°C, (Note 3)	Line Regulation	$\Delta V_{O}$
V	-17.5)	$\leq V_{IN} \leq -$	(-30	–14.5)	$\leq V_{IN} \leq 1$	(-30 ±			
mV	50	3		30	3				
V	-20)	$S \leq V_{IN} \leq$	(-26	-16)	≤ V <sub>IN</sub> ≤	(-22			
-		Ū	(–26		Ū	(–22			

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#### **Electrical Characteristics** (Continued)

Conditions unless otherwise noted:  $I_{OUT} = 500 mA$ ,  $C_{IN} = 2.2 \mu F$ ,  $C_{OUT} = 1 \mu F$ ,  $0^{\circ}C \le T_{J} \le +125^{\circ}C$ , Power Dissipation  $\le 1.5 W$ .

Part Number		LM7912C			LM7915C			Units	
Output Voltage		-12V			-15V				
	Input Voltage (unless	otherwise specified)	-19V -23V						
Symbol	Parameter	Conditions	Min	Тур	Max	Min	Тур	Max	
$\Delta V_{O}$	Load Regulation	T <sub>J</sub> = 25°C, (Note 3)							
		$5mA \le I_{OUT} \le 1.5A$		15	200		15	200	mV
		$250\text{mA} \le I_{OUT} \le 750\text{mA}$		5	75		5	75	mV
I <sub>Q</sub>	Quiescent Current	$T_J = 25^{\circ}C$		1.5	3		1.5	3	mA
$\Delta I_Q$	Quiescent Current	With Line			0.5			0.5	mA
	Change		(-30 s	≤ V <sub>IN</sub> ≤	-14.5)	(-30	$\leq V_{IN} \leq -$	-17.5)	V
		With Load, $5mA \le I_{OUT} \le 1A$			0.5			0.5	mA
V <sub>n</sub>	Output Noise Voltage	$T_A = 25^{\circ}C, 10Hz \le f \le 100Hz$		300			375		μV
	Ripple Rejection	f = 120 Hz	54	70		54	70		dB
			(-25	≤ V <sub>IN</sub> ≤	-15)	(-30	$\leq V_{IN} \leq -$	-17.5)	V
	Dropout Voltage	$T_{J} = 25^{\circ}C, I_{OUT} = 1A$		1.1			1.1		V
I <sub>OMAX</sub>	Peak Output Current	$T_J = 25^{\circ}C$		2.2			2.2		А
	Average Temperature	I <sub>OUT</sub> = 5mA,		-0.8			-1.0		mV/°C
	Coefficient of	$0 \text{ C} \leq T_J \leq 100^{\circ}\text{C}$							
	Output Voltage								

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not guarantee Specific Performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics.

Note 2: Refer to Typical Performance Characteristics and Design Considerations for details.

Note 3: Regulation is measured at a constant junction temperature by pulse testing with a low duty cycle. Changes in output voltage due to heating effects must be taken into account.

### **Design Considerations**

The LM79XX fixed voltage regulator series has thermal overload protection from excessive power dissipation, internal short circuit protection which limits the circuit's maximum current, and output transistor safe-area compensation for reducing the output current as the voltage across the pass transistor is increased.

Although the internal power dissipation is limited, the junction temperature must be kept below the maximum specified temperature (125°C) in order to meet data sheet specifications. To calculate the maximum junction temperature or heat sink required, the following thermal resistance values should be used:

	Тур	Max	Тур	Max
Package	θις	$\theta$ JC	$\theta_{JA}$	$\theta_{JA}$
	°C/W	°C/W	°C/W	°C/W
TO-220	3.0	5.0	60	40

$$\begin{aligned} & P_{\text{D MAX}} = \frac{T_{\text{J Max}} - T_{\text{A}}}{\theta_{\text{JC}} + \theta_{\text{CA}}} \text{ or } \frac{T_{\text{J Max}} T_{\text{A}}}{\theta_{\text{JA}}} \\ & \theta_{\text{CA}} = \theta_{\text{CS}} + \theta_{\text{SA}} \text{ (without heat sink)} \end{aligned}$$

Solving for T<sub>J</sub>:

$$T_J = T_A + P_D (\theta_{JC} + \theta_{CA})$$
 or  
=  $T_A + P_D \theta_{JA}$  (without heat sink)

Where:

T<sub>J</sub> = Junction Temperature

T<sub>A</sub> = Ambient Temperature

P<sub>D</sub> = Power Dissipation

 $\theta_{JA}$  = Junction-to-Ambient Thermal Resistance

 $\theta_{\text{JC}}$  = Junction-to-Case Thermal Resistance

 $\theta_{CA}$  = Case-to-Ambient Thermal Resistance

 $\theta_{CS}$  = Case-to-Heat Sink Thermal Resistance

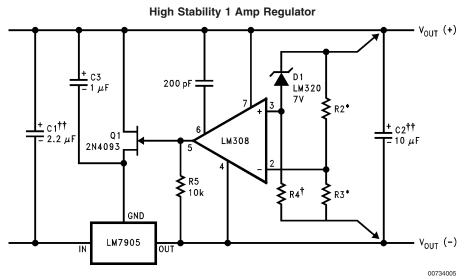
 $\theta_{SA}$  = Heat Sink-to-Ambient Thermal Resistance

## **Typical Applications**

Bypass capacitors are necessary for stable operation of the LM79XX series of regulators over the input voltage and output current ranges. Output bypass capacitors will improve the transient response by the regulator.

The bypass capacitors, (2.2 $\mu$ F on the input, 1.0 $\mu$ F on the output) should be ceramic or solid tantalum which have good

high frequency characteristics. If aluminum electrolytics are used, their values should be  $10\mu F$  or larger. The bypass capacitors should be mounted with the shortest leads, and if possible, directly across the regulator terminals.

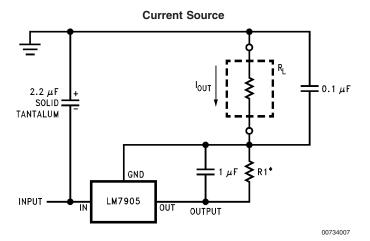


Load and line regulation < 0.01% temperature stability  $\leq 0.2\%$ 

†Determine Zener current

††Solid tantalum

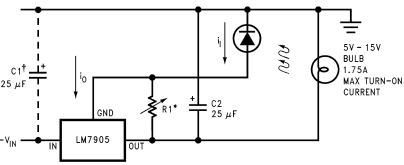
\*Select resistors to set output voltage. 2 ppm/°C tracking suggested



 $*I_{OUT} = 1 \text{ mA} + \frac{5V}{R1}$ 

## Typical Applications (Continued)

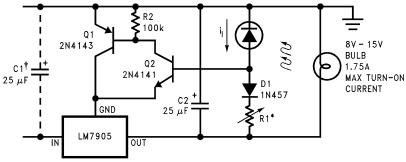
#### **Light Controller Using Silicon Photo Cell**



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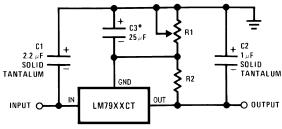
 $<sup>^\</sup>dagger \text{Necessary}$  only if raw supply filter capacitor is more that 2" from LM7905CT





00734009

#### **Variable Output**



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$$V_{OUT} = V_{SET} \left( \frac{R1 + R2}{R2} \right)$$

Select R2 as follows:

 $\begin{array}{ll} \text{LM7905CT} & 300\Omega \\ \text{LM7912CT} & 750\Omega \\ \text{LM7915CT} & 1\text{k} \end{array}$ 

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<sup>\*</sup>Lamp brightness increase until  $i_{l} \! = i_{Q} \; (\approx 1 \text{ mA}) \, + \, 5 \text{V/R1} \, .$ 

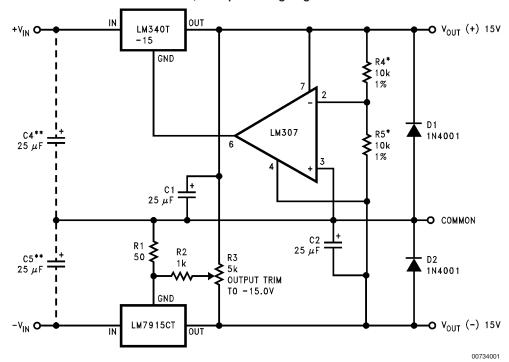
<sup>\*</sup>Lamp brightness increases until  $i_i$  = 5V/R1 ( $I_i$  can be set as low as 1  $\mu$ A)

 $<sup>\</sup>dagger \text{Necessary}$  only if raw supply filter capacitor is more that 2" from LM7905

<sup>\*</sup>Improves transient response and ripple rejection. Do not increase beyond 50  $\mu\text{F}.$ 

# Typical Applications (Continued)

#### ±15V, 1 Amp Tracking Regulators



	(-15)	(+15)
Load Regulation at $\Delta I_L = 1A$	40mV	2mV
Output Ripple, $C_{IN} = 3000 \mu F$ , $I_L = 1A$	100 μVms	100 μVms
Temperature Stability	50mV	50mV
Output Noise $10Hz \le f \le 10kHz$	150 μVms	150 μVms

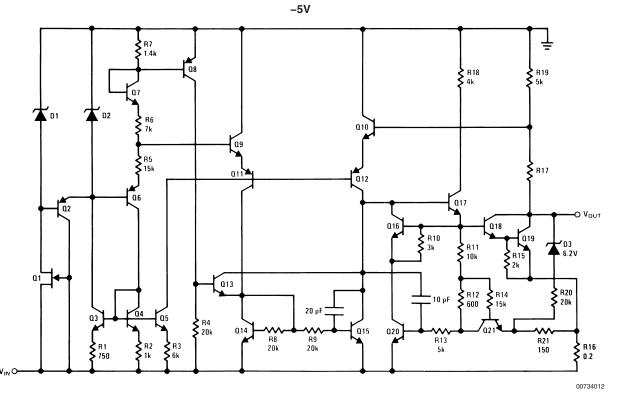
<sup>\*</sup>Resistor tolerance of R4 and R5 determine matching of (+) and (-) outputs.

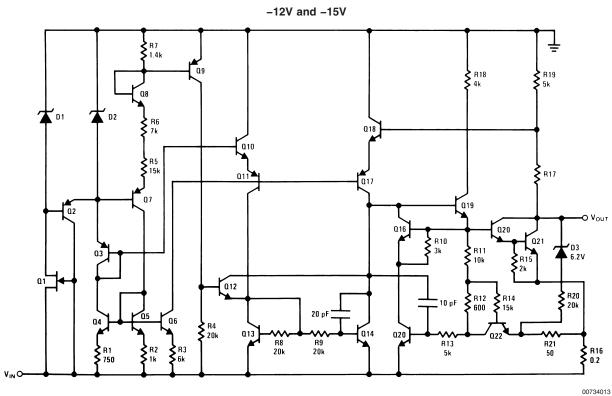
#### **Dual Trimmed Supply** +INPUT O **O** +5.0V OUT GND 240 D1 1N4001 $0.22~\mu F$ **≶** 33 • сом D2 1N4001 $2.2~\mu F$ 470 **≨** 5k GND -INPUT O LM7905 -5.0V OUT 00734004

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<sup>\*\*</sup>Necessary only if raw supply filter capacitors are more than 3" from regulators.

# **Schematic Diagrams**

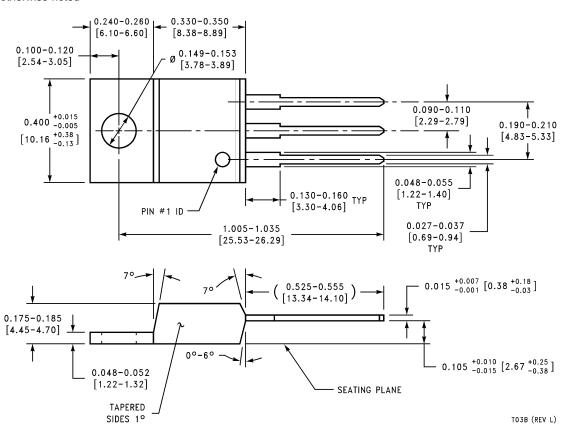




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## Physical Dimensions inches (millimeters)

unless otherwise noted



TO-220 Outline Package (T)
Order Number LM7905CT, LM7912CT or LM7915CT
NS Package Number T03B

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